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Attn: Jacqueline J. Garner
12/12/2005 TBESHAW2 00000002 200668 10649425

01 FC:1501 1400.00 DA

02 FC:1504 1400.00 DA FILING DATE

FIRST NAMED INVENTOR

ATTORNEY DOCKET NO.

CONFIRMATION NO.

10/649,425

08/27/2003

James J. Chambers

TI35714

5169

TITLE OF INVENTION: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED TRANSISTORS WITH DUAL WORK FUNCTION METAL GA ELECTRODES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	12/29/2005
EXAMINER		ART UNIT	CLASS-SUBCLASS		
SARKAR, ASOK K		2891	438-585000		

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1 Jacqueline J. Garner
2 W. James Brady III
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Texas Instruments Incorporated

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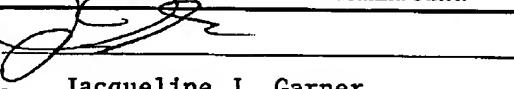
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Date 12/08/05

Typed or printed name Jacqueline J. Garner

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James J. Chambers
Serial No: 10/649,425
Filed: 08/27/2003
Art Unit: 2891
Examiner: Asok K. Sarkar
Docket No.: TI-35714
Conf. No.: 5169
Customer No.: 23494

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NAME OF INVENTOR(S):	
James J. Chambers	
TITLE OF INVENTION:	
FERAM Capacitor Stack Etch	
TI FILE NO.:	DEPOSIT ACCT. NO.:
TI-35714	20-0668
FAXED: 12/9/2005	
DUE: 12/29/2005	
ATTY/SECY: JJG/ms	
RECEIPT DATE & SERIAL NO.:	
Serial No.: 10/649,425	
Filing Date: 8/27/2003	
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